

90~

PIN 1: VEET	~100a
PIN 2: TX FAULT	~100b
PIN 3: TX DISABLE	~100c
PIN 4: MOD-DEF(2)	~100d
PIN 5: MOD-DEF(1)	~100e
PIN 6: MOD-DEF(0)	~100f
PIN 7: RATE-SELECT	~100g
PIN 8: LOS	~100h
PIN 9: VEER	~100i
PIN 10: VEER	~100j
PIN 11: VEER	~100k
PIN 12: RD-	~100l
PIN 13: RD+	~100m
PIN 14: VEER	~100n
PIN 15: VCCR	~100o
PIN 16: VCCT	~100p
PIN 17: VEET	~100q
PIN 18: TD+	~100r
PIN 19: TD-	~100s
PIN 20: VEET	~100t

Fig. /

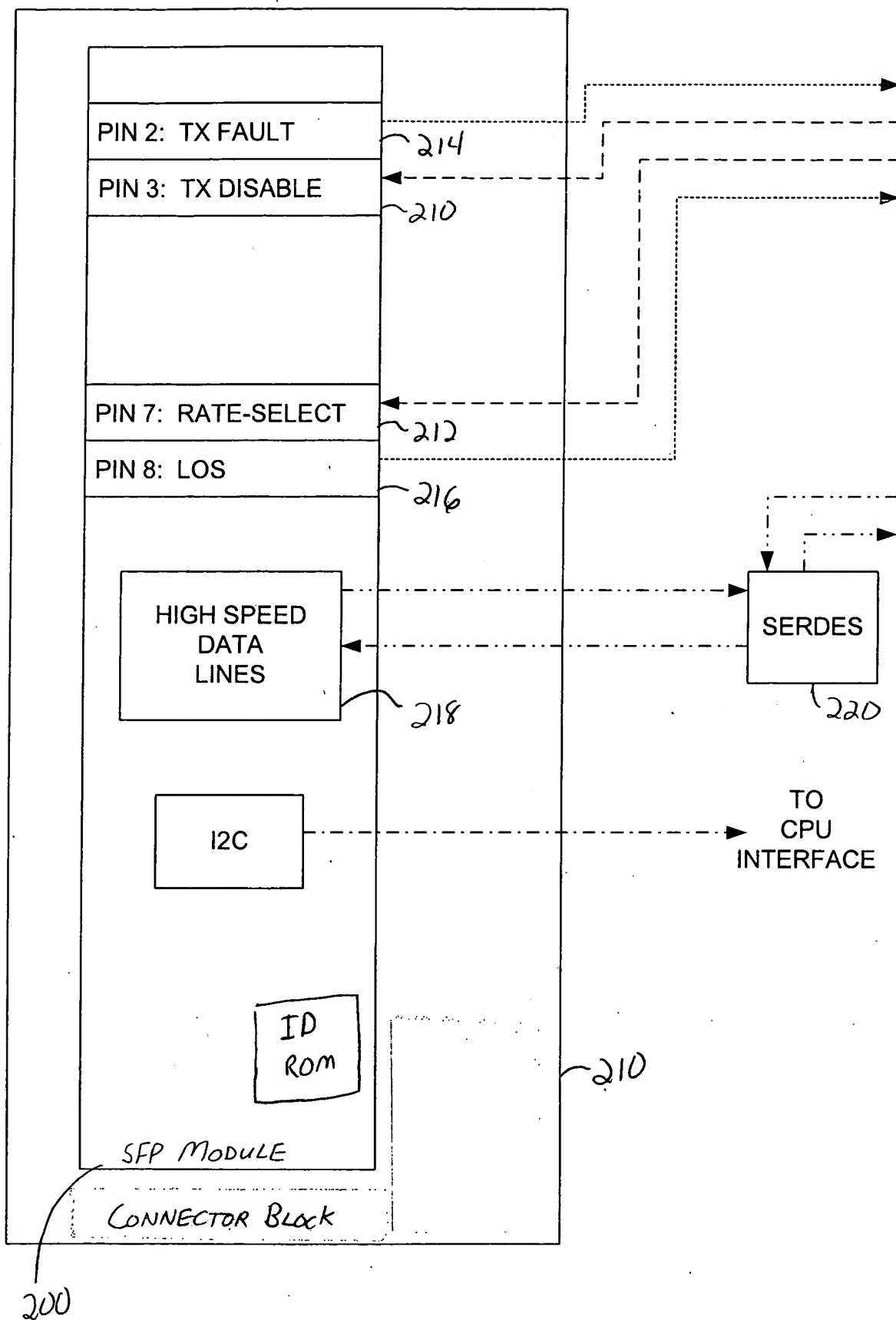


Fig. 2

304~

PIN 1: VEET	~324
PIN 2: TX FAULT OR RX DATA	~308
PIN 3: TX DISABLE OR TX CLOCK	~312
PIN 4: MOD-DEF(2)	~324
PIN 5: MOD-DEF(1)	~324
PIN 6: MOD-DEF(0)	~324
PIN 7: RATE-SELECT OR TX DATA	~316
PIN 8: LOS OR RX CLOCK/LOS	~320
PIN 9: VEER	~324
PIN 10: VEER	~324
PIN 11: VEER	~324
PIN 12: RD-	~324
PIN 13: RD+	~324
PIN 14: VEER	~324
PIN 15: VCCR	~324
PIN 16: VCCT	~324
PIN 17: VEET	~324
PIN 18: TD+	~324
PIN 19: TD-	~324
PIN 20: VEET	~324

Fig. 3

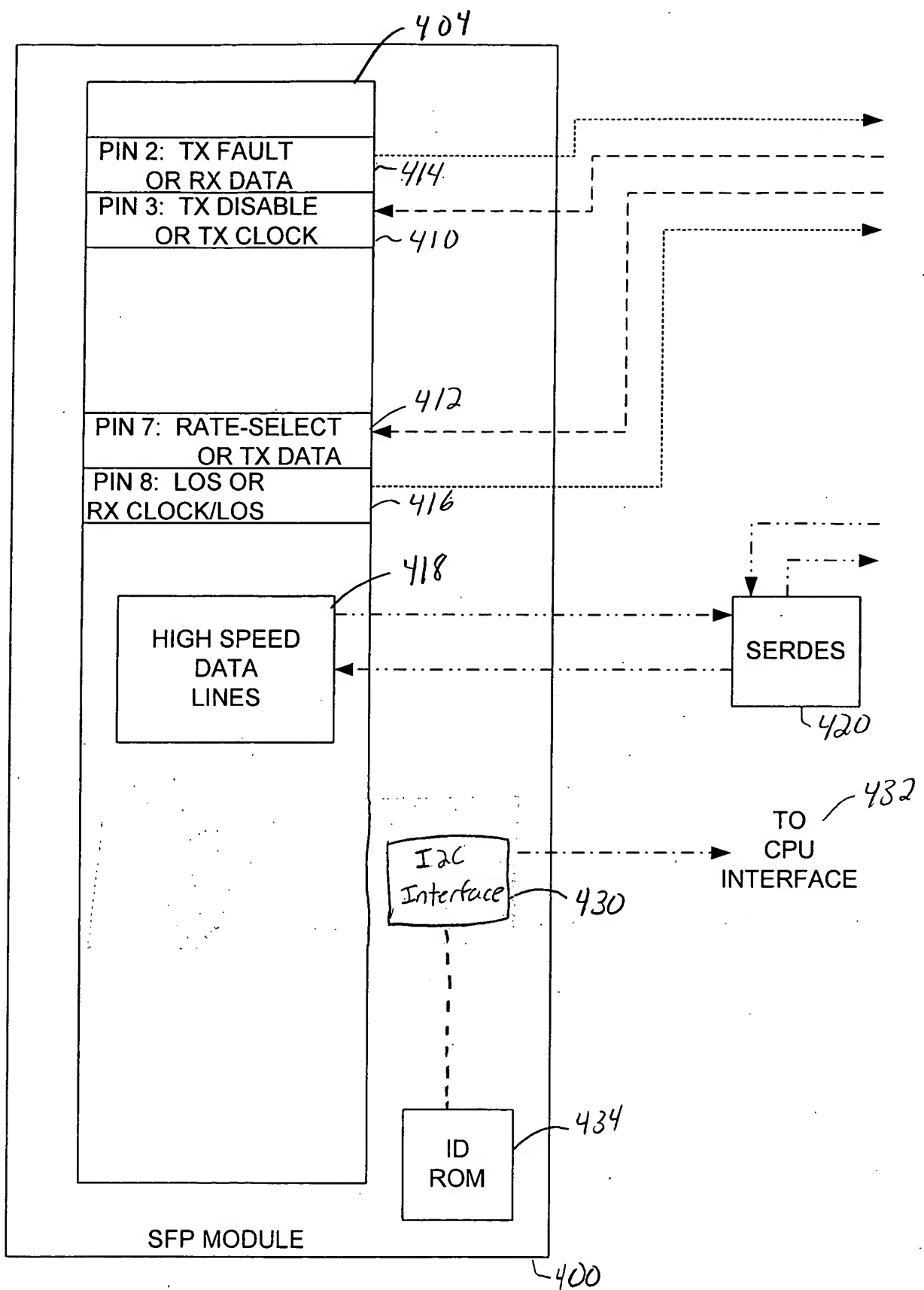


Fig. 4

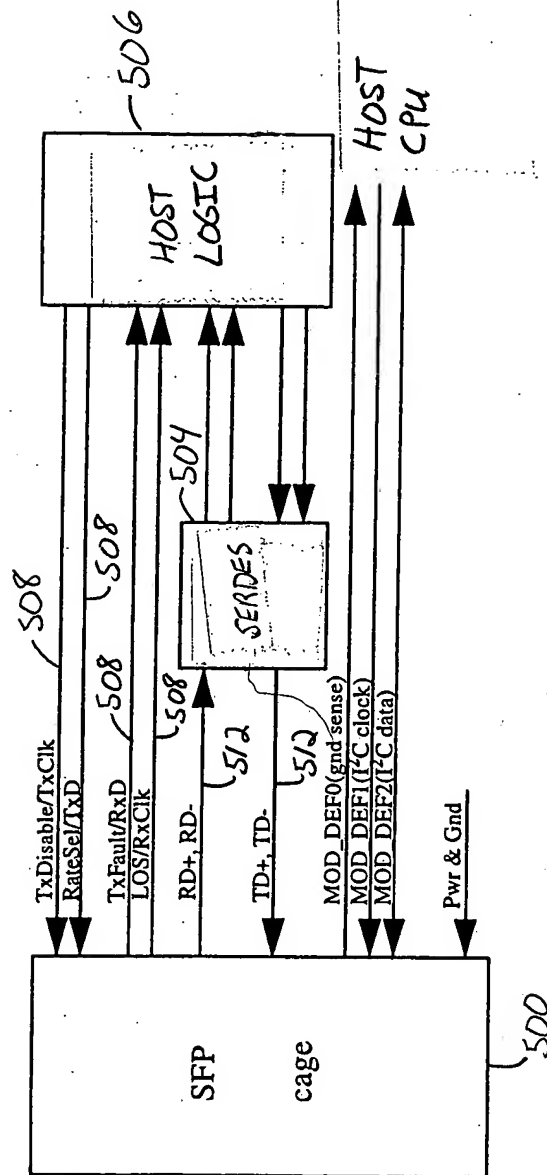


Fig. 5

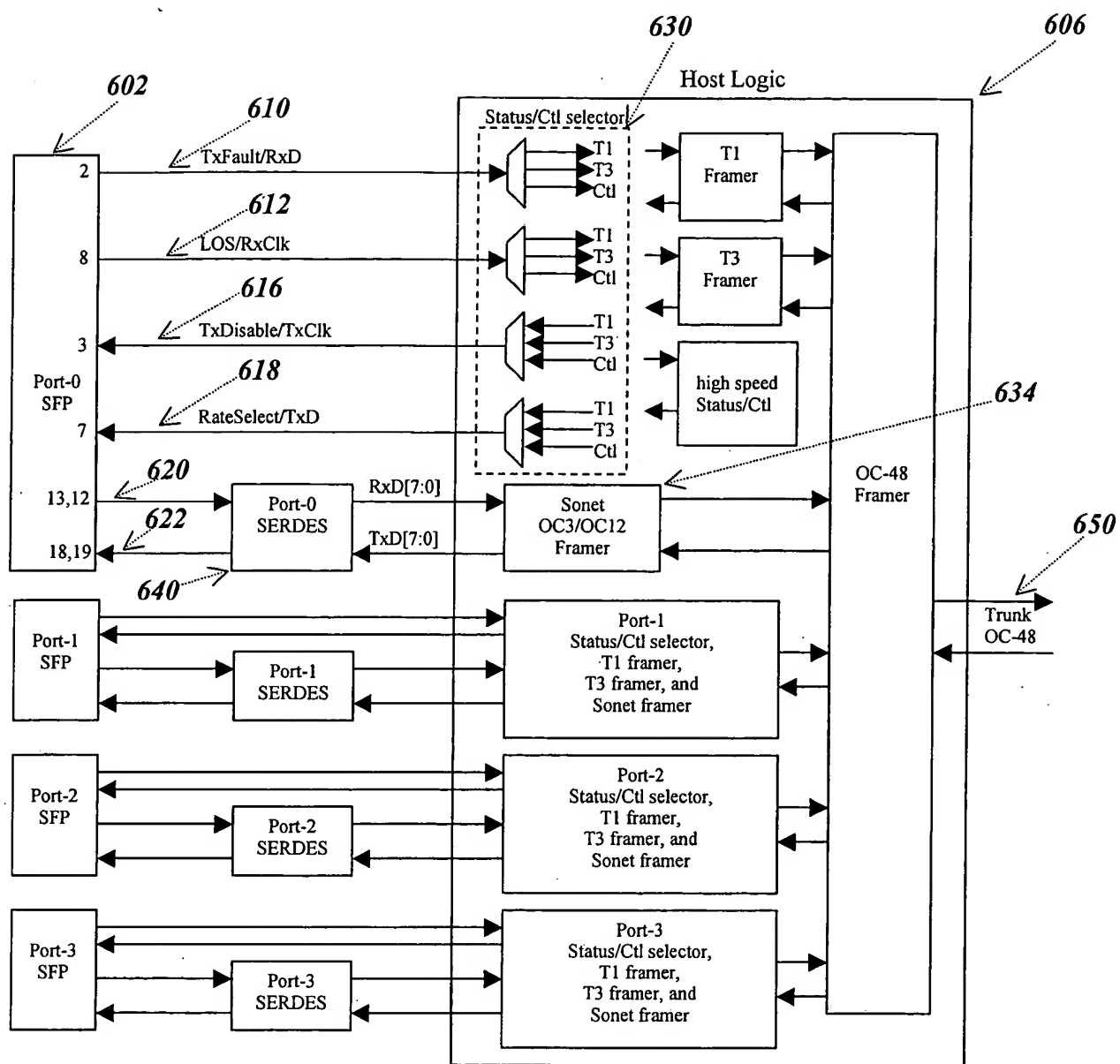


Fig. 6

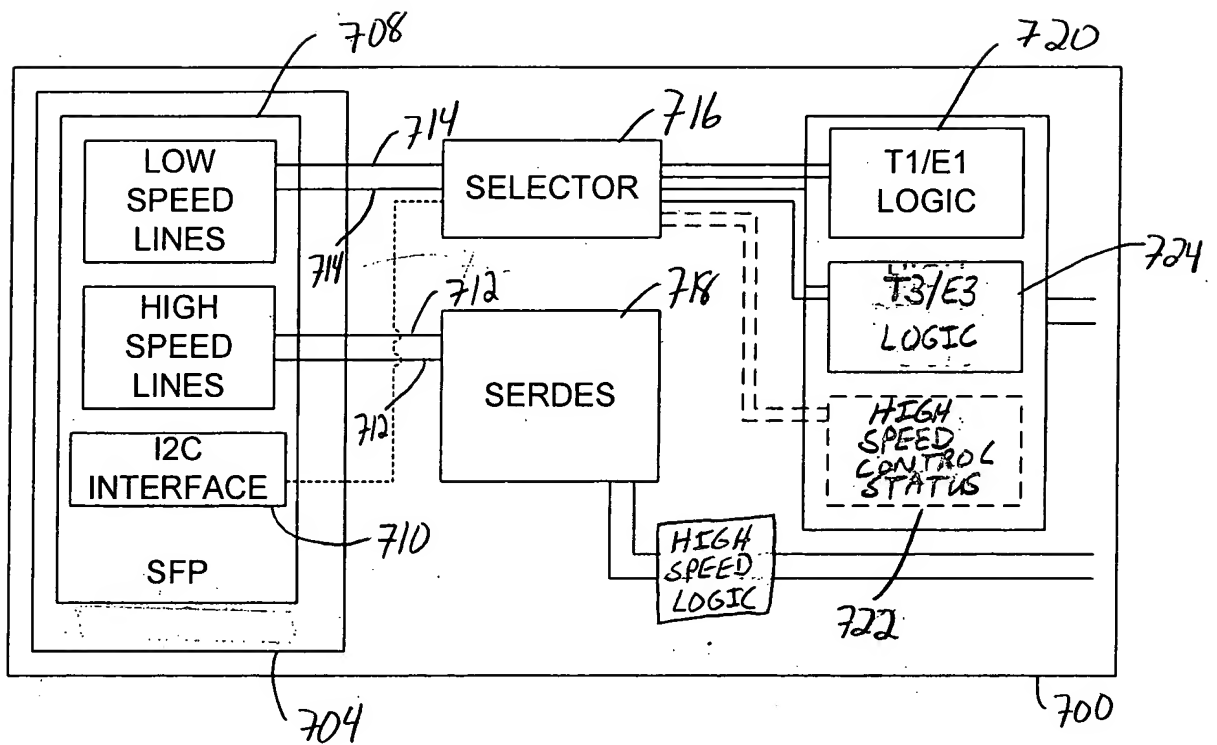


Fig. 7

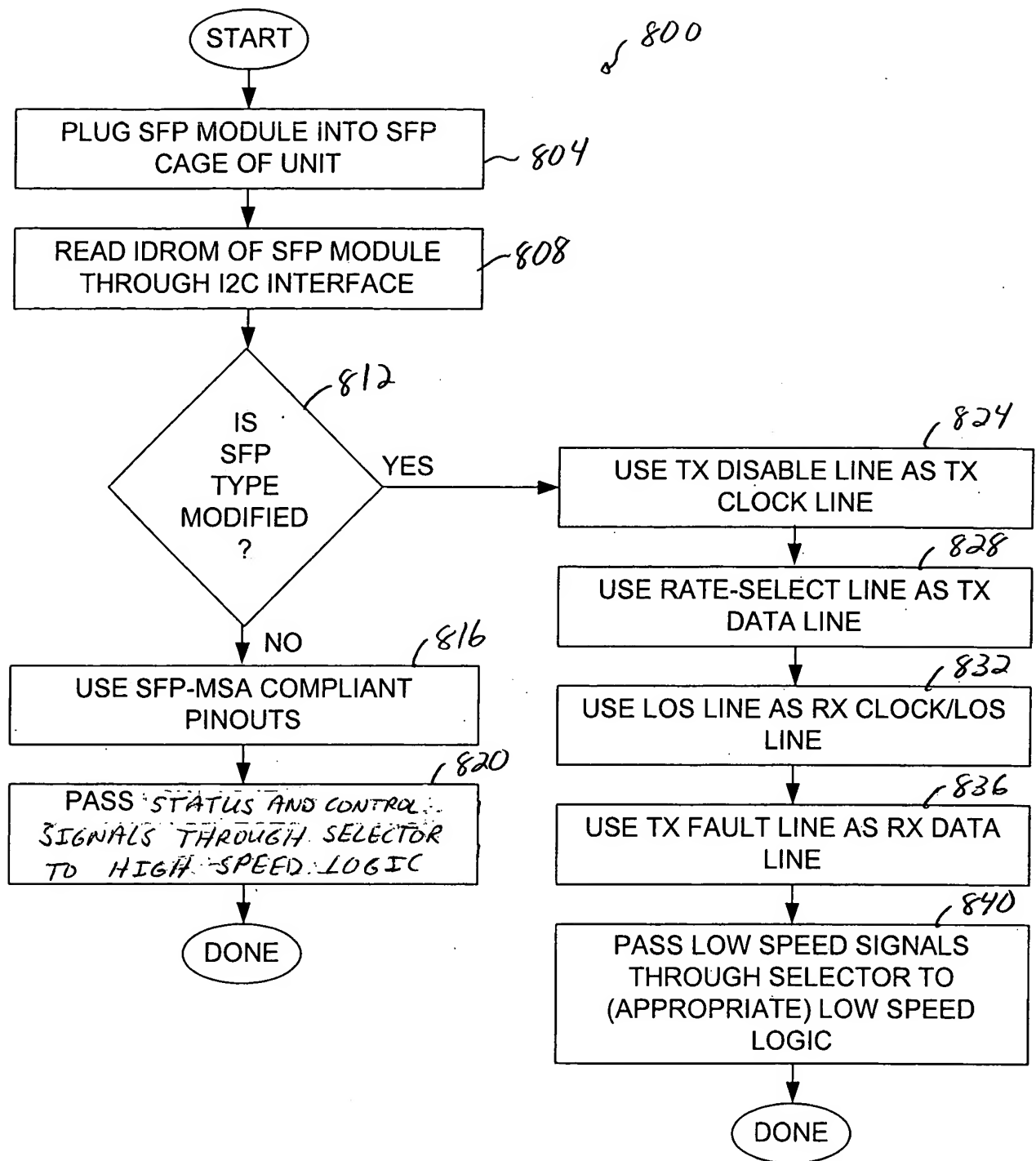


Fig. 8

1030~

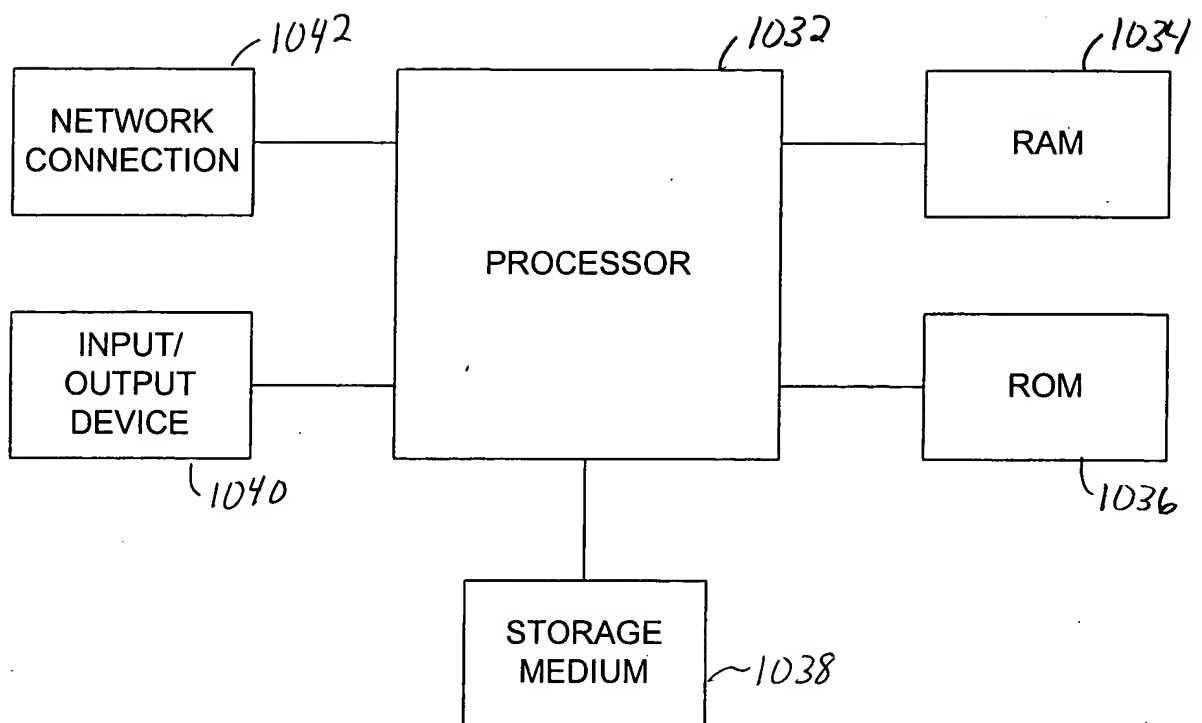


Fig. 9